

FIGURE 1 (Prior Art)

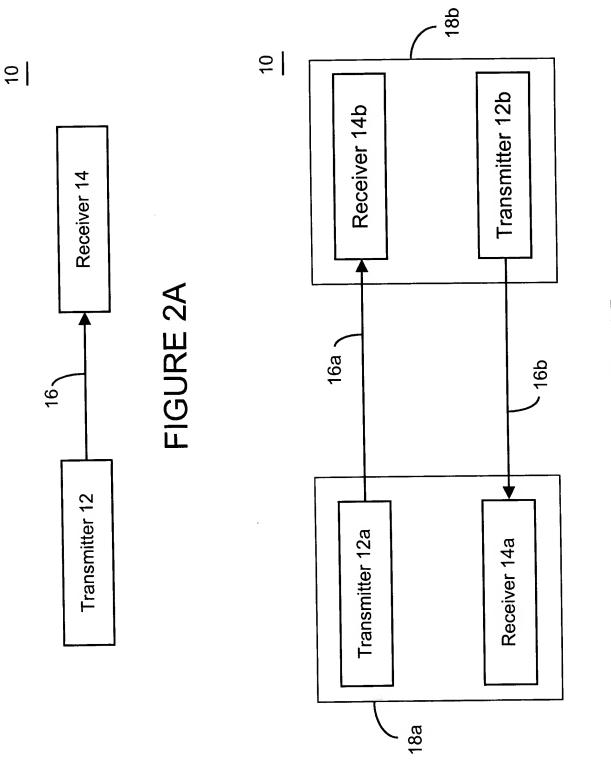
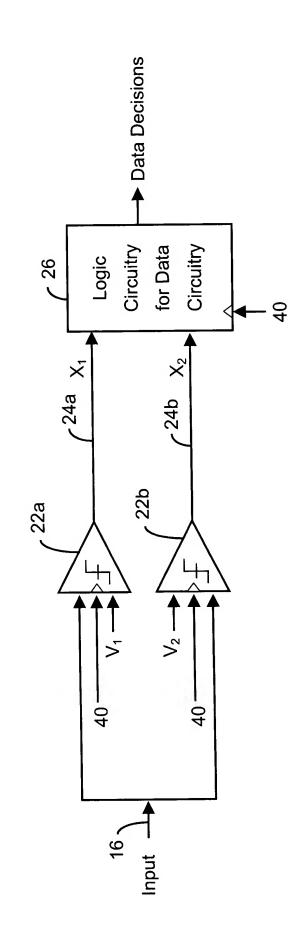
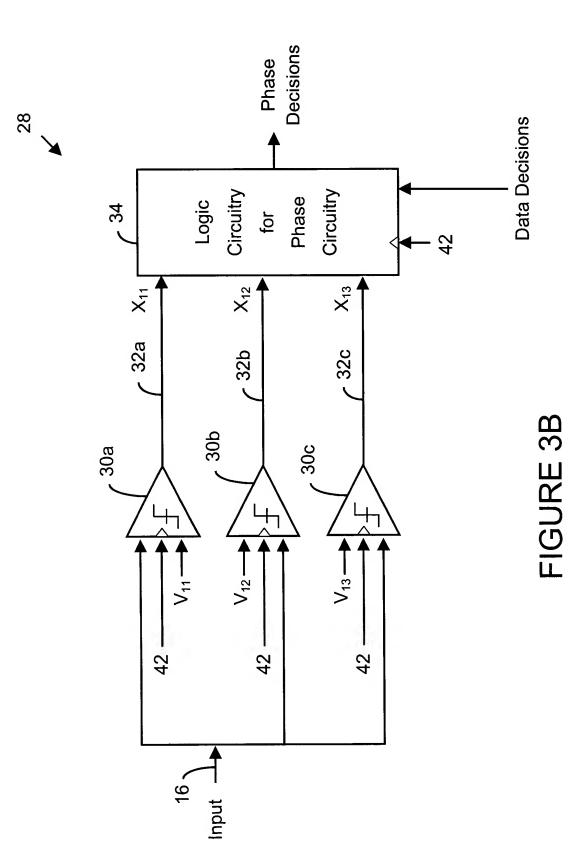


FIGURE 2B



20

FIGURE 3A



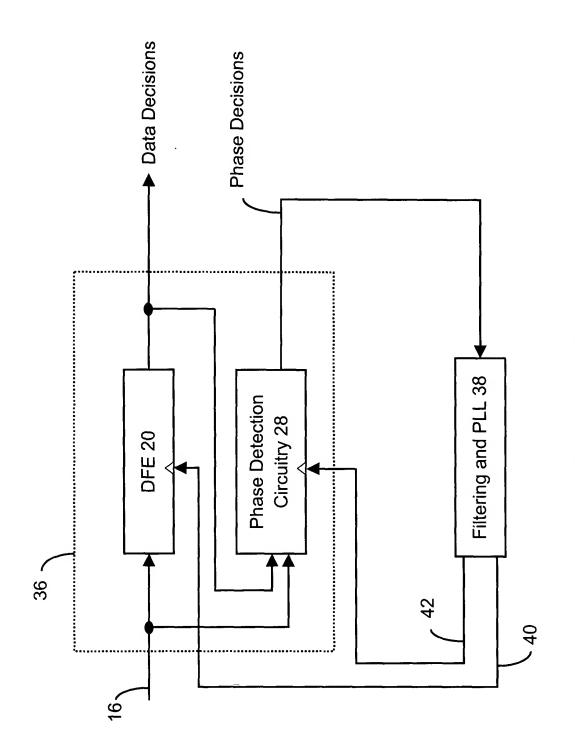
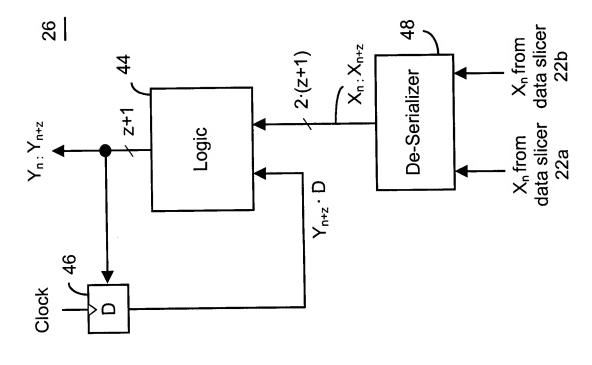


FIGURE 3C



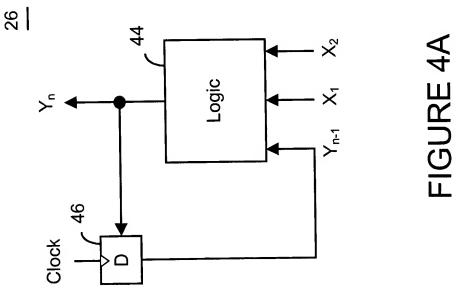


FIGURE 4B

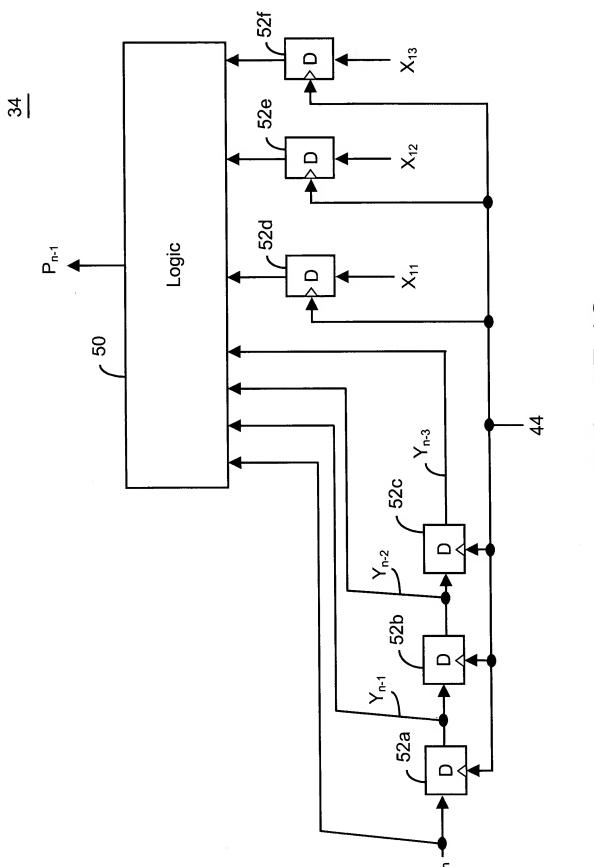
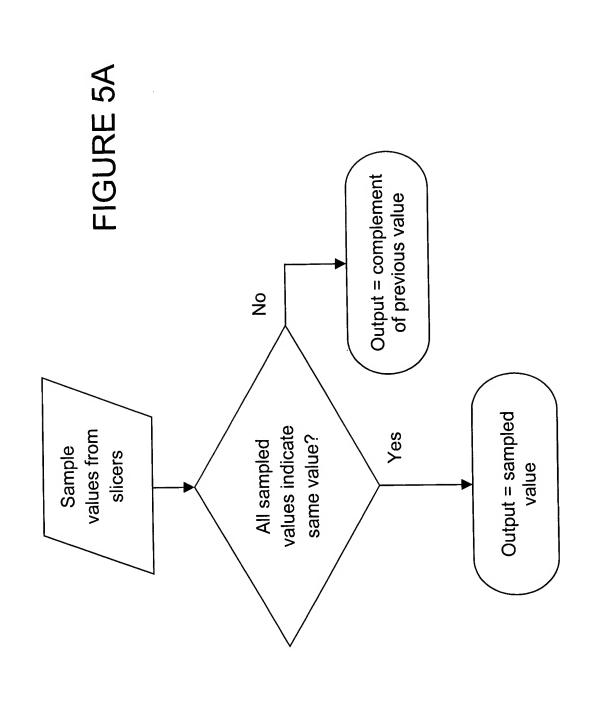
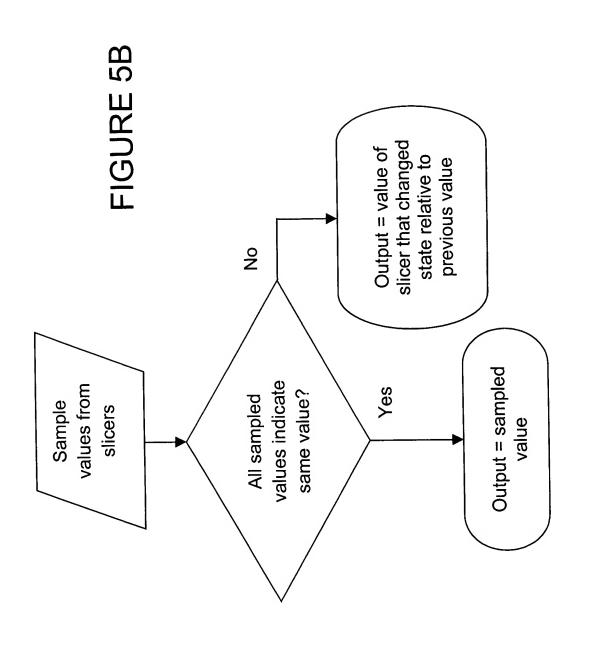
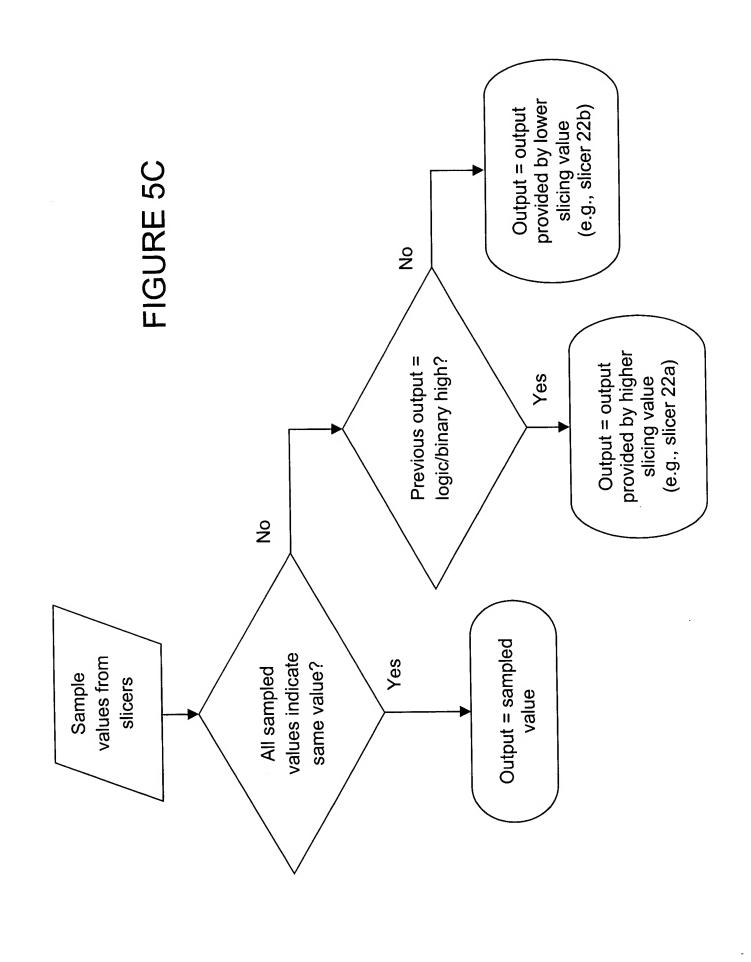
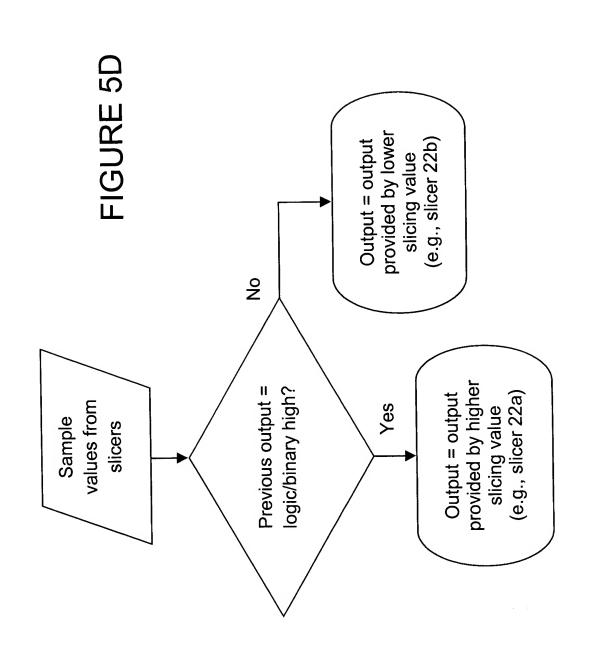


FIGURE 4C









54

Memory

FIGURE 6A

FIGURE 6B

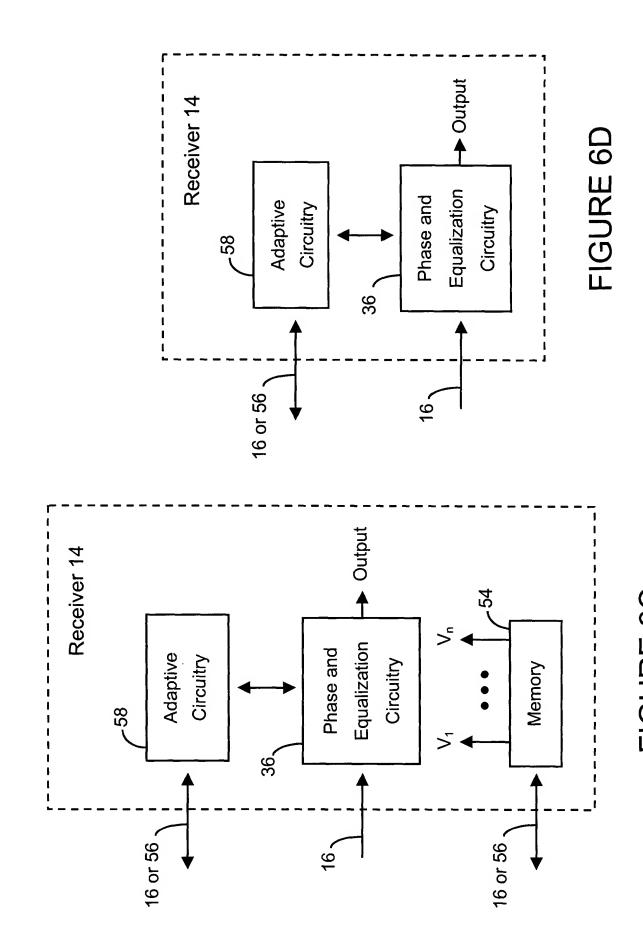


FIGURE 6C

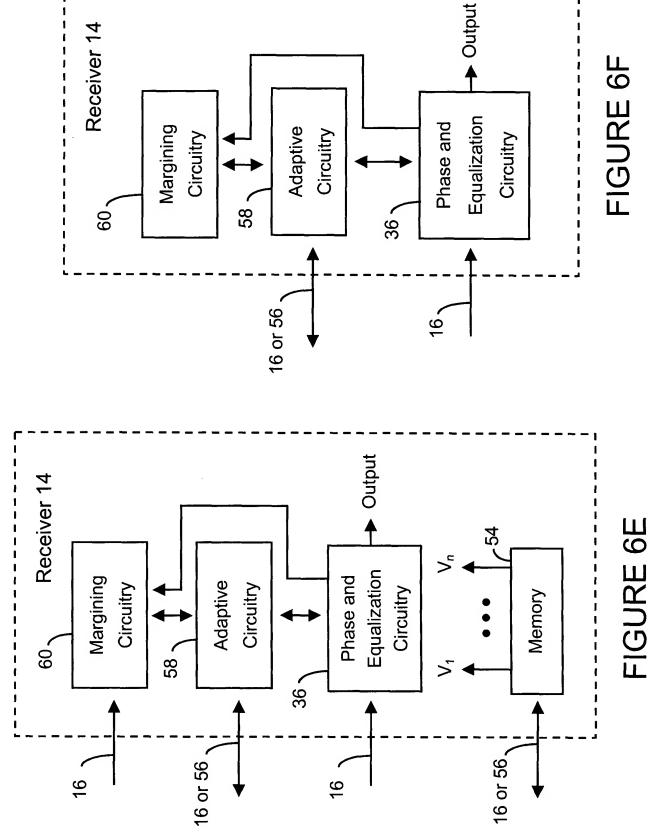
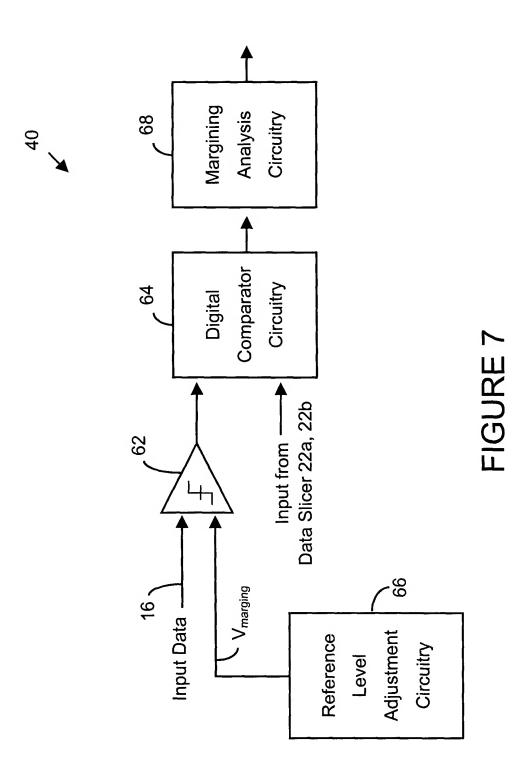


FIGURE 6F



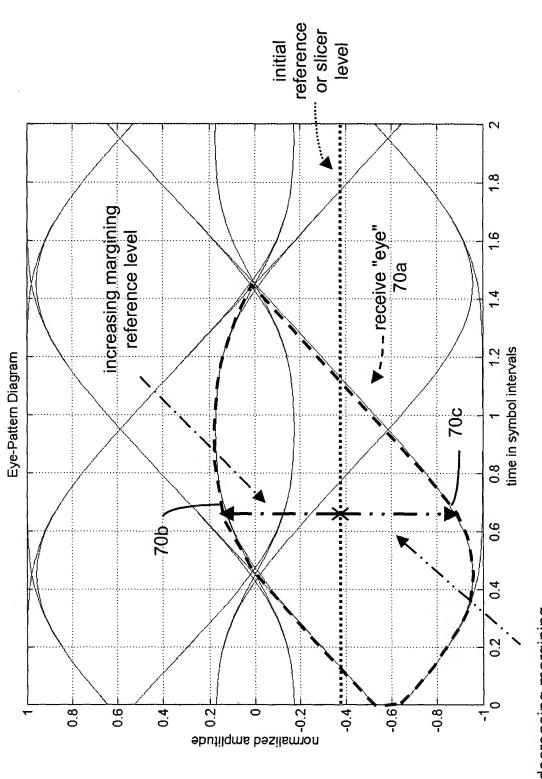


FIGURE 8A

decreasing margining reference level

FIGURE 8B

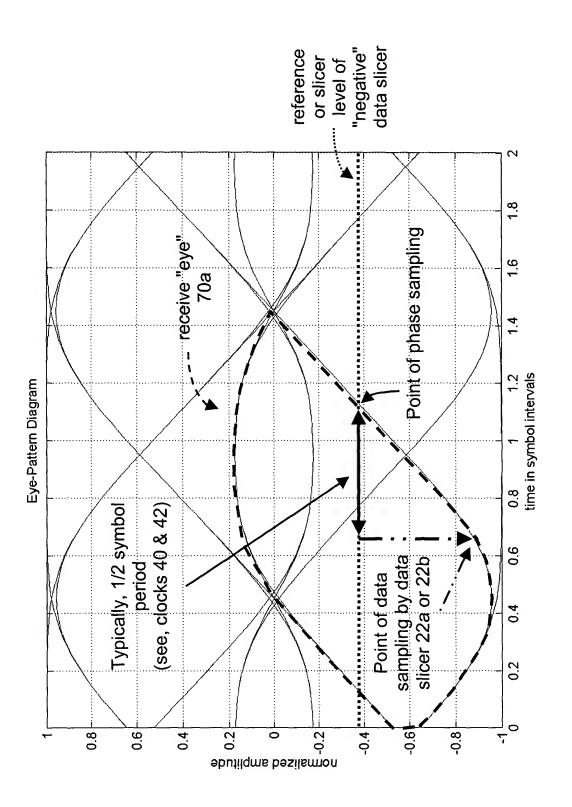


FIGURE 8C

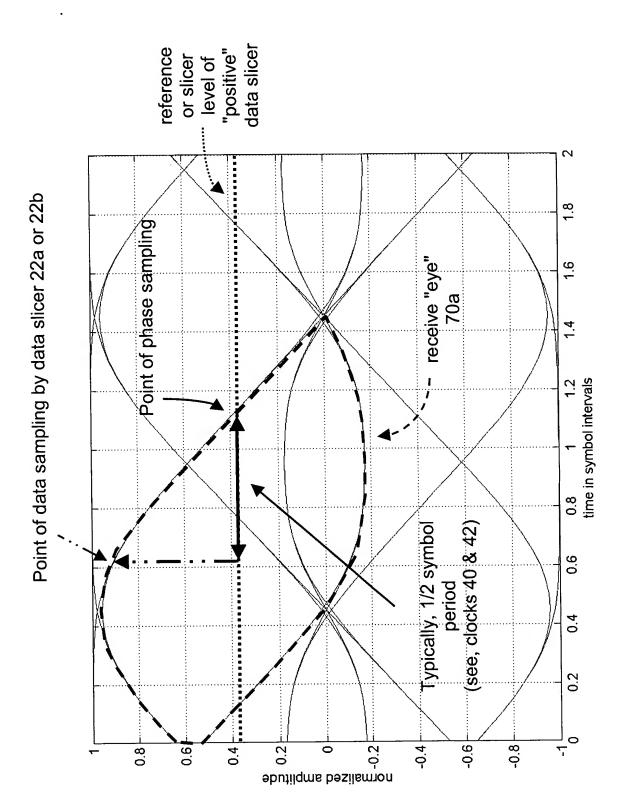


FIGURE 8D

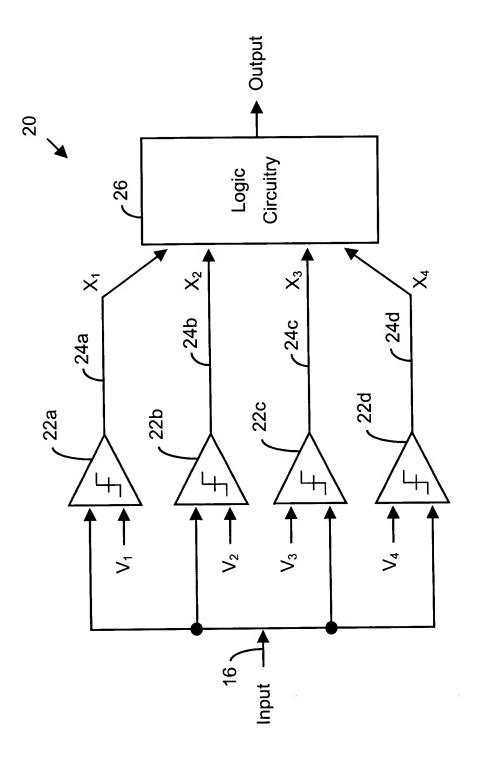


FIGURE 9A

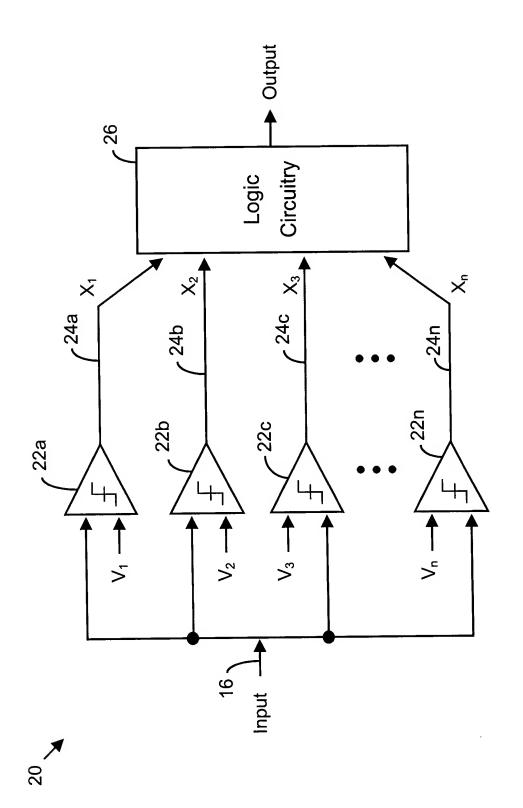


FIGURE 9B

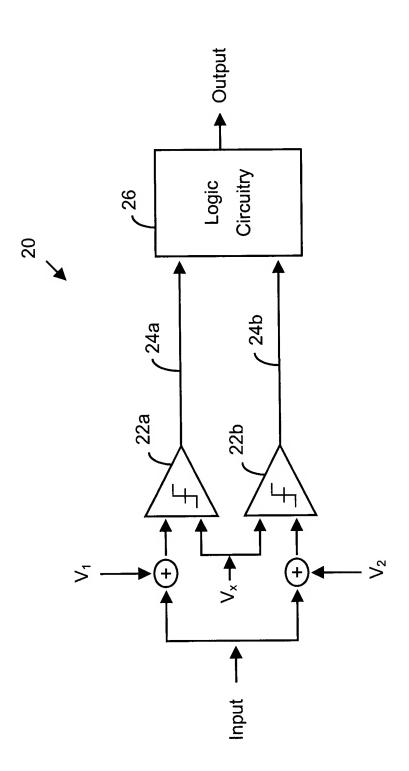


FIGURE 10

